



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

ERIC SOENEN, ET AL.

Application No.: 10/656,793

Filed: September 5, 2003

For: **Improved Capacitor Structure And
Automated Design Flow For Incorporating
Same**

Art Group: 2826

Examiner: Kevin V. Quinto

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

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Date: 4/12/05

Respectfully submitted,

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Janece Shannon
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4/12/2005

04-12-05

Date

STATEMENT BY APPLICANT

(use as many sheets as necessary)

Application Number	10/656,793
Filing Date	09/05/2003
First Named Inventor:	Soenen, et al.
Art Unit	2826
Examiner Name	Kevin Quinto
Attorney Docket Number	4363.P015

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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

Examiner Signature		Date Considered	
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